

FIG. 1
ADAPTIVE COMPUTING ENGINE (ACE)

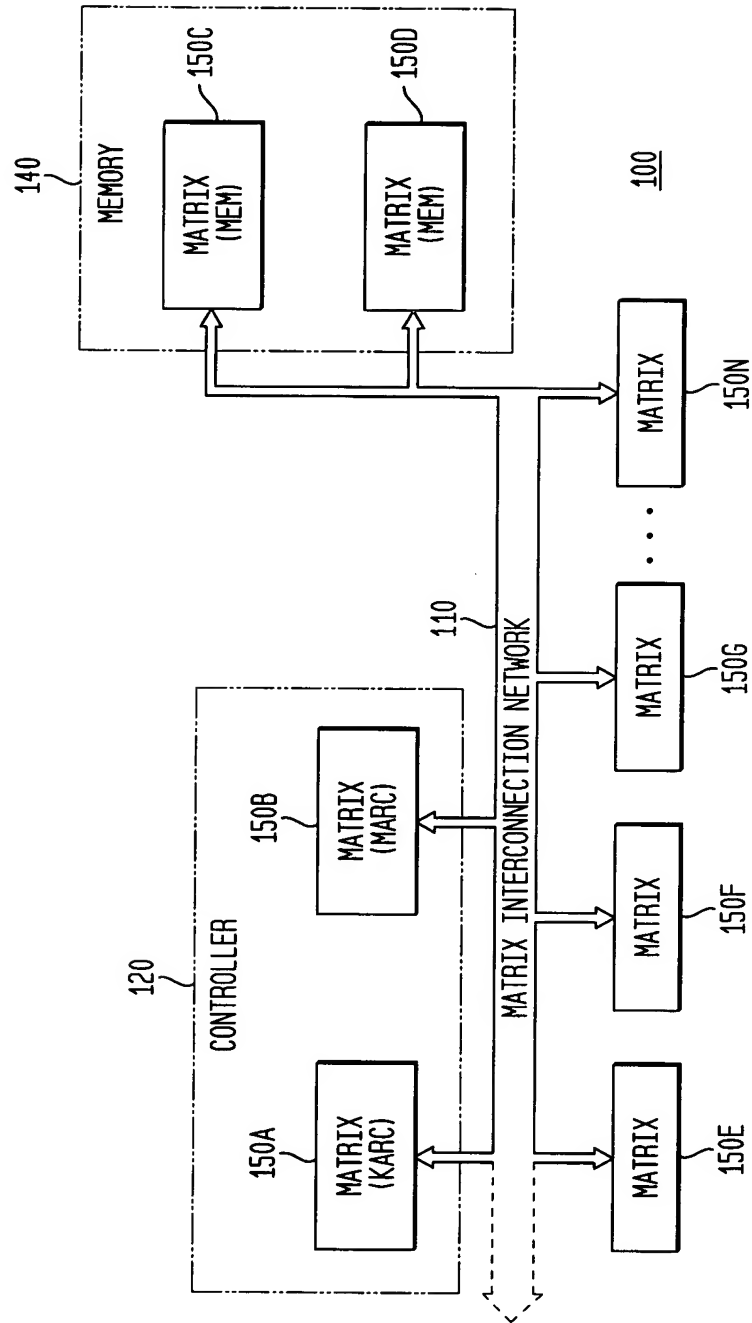


FIG. 2

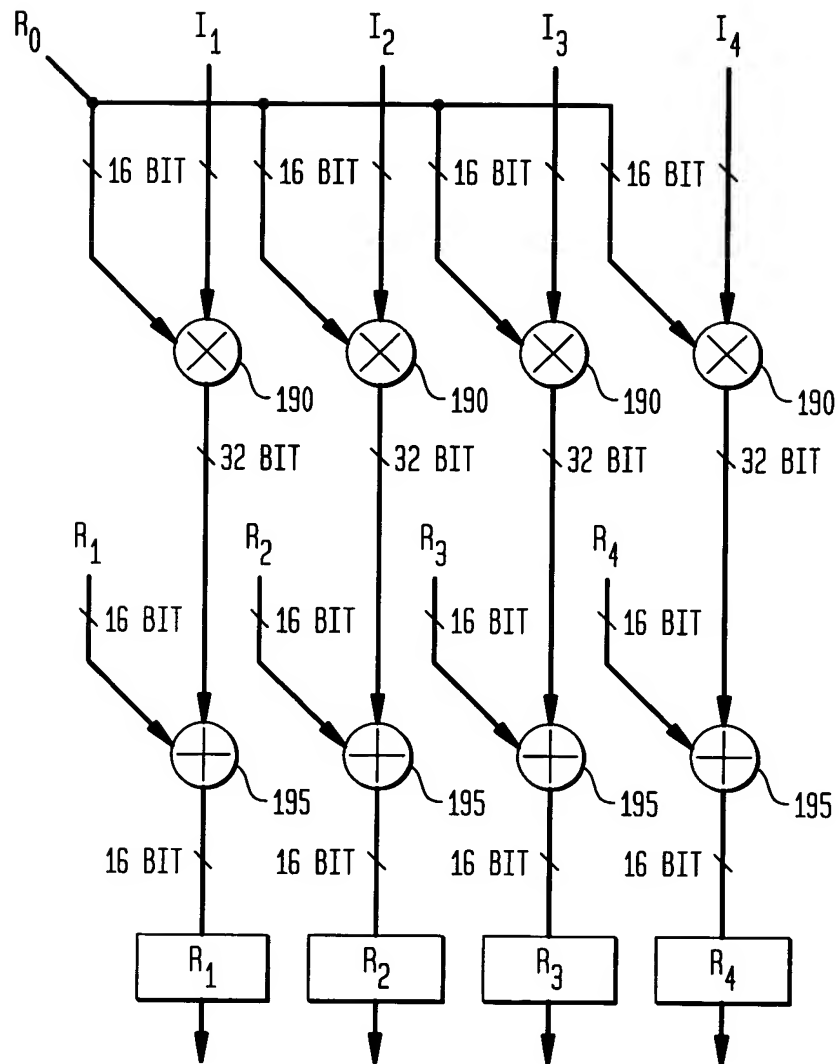


FIG. 3

TO OTHER MATRICES 150
(INCLUDING CONTROLLER 120
AND MEMORY 140)

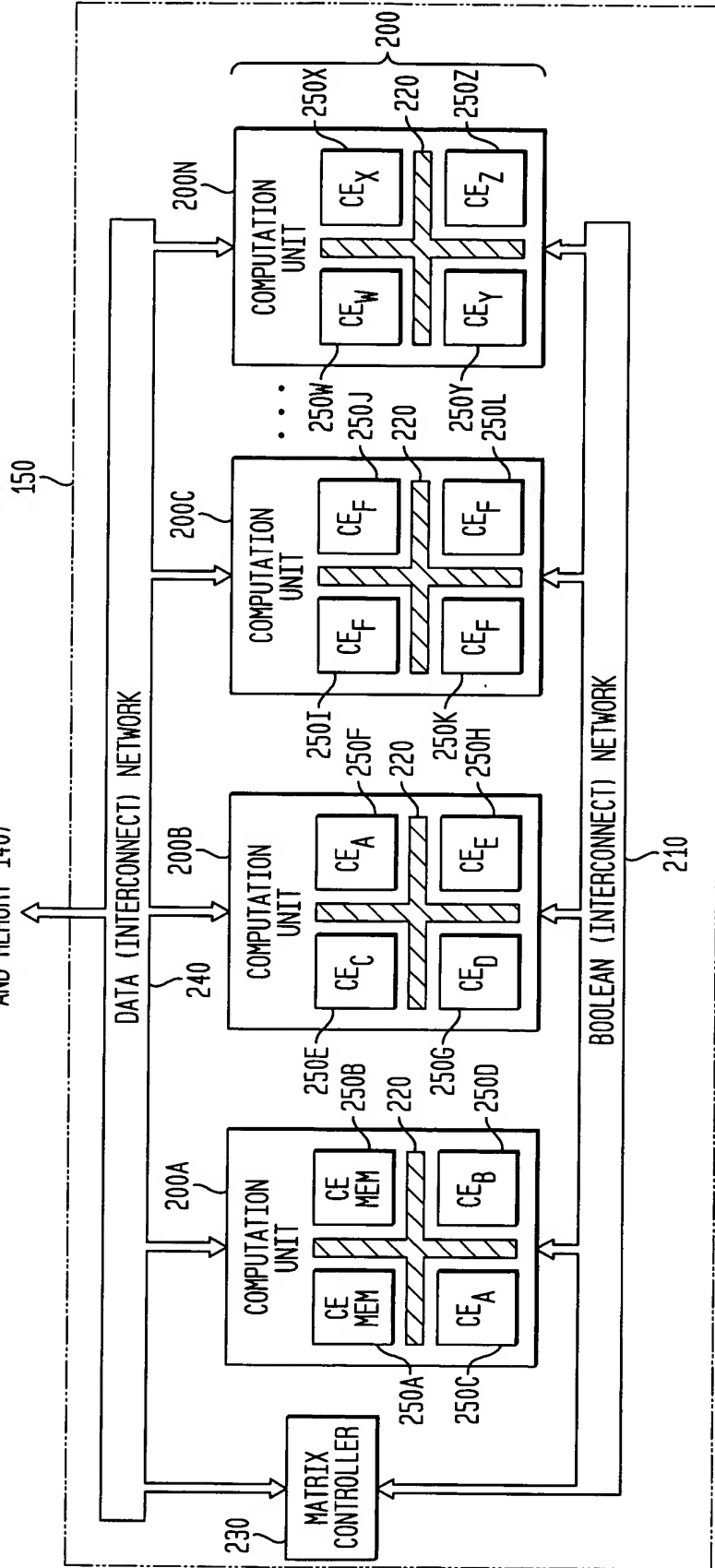


FIG. 4

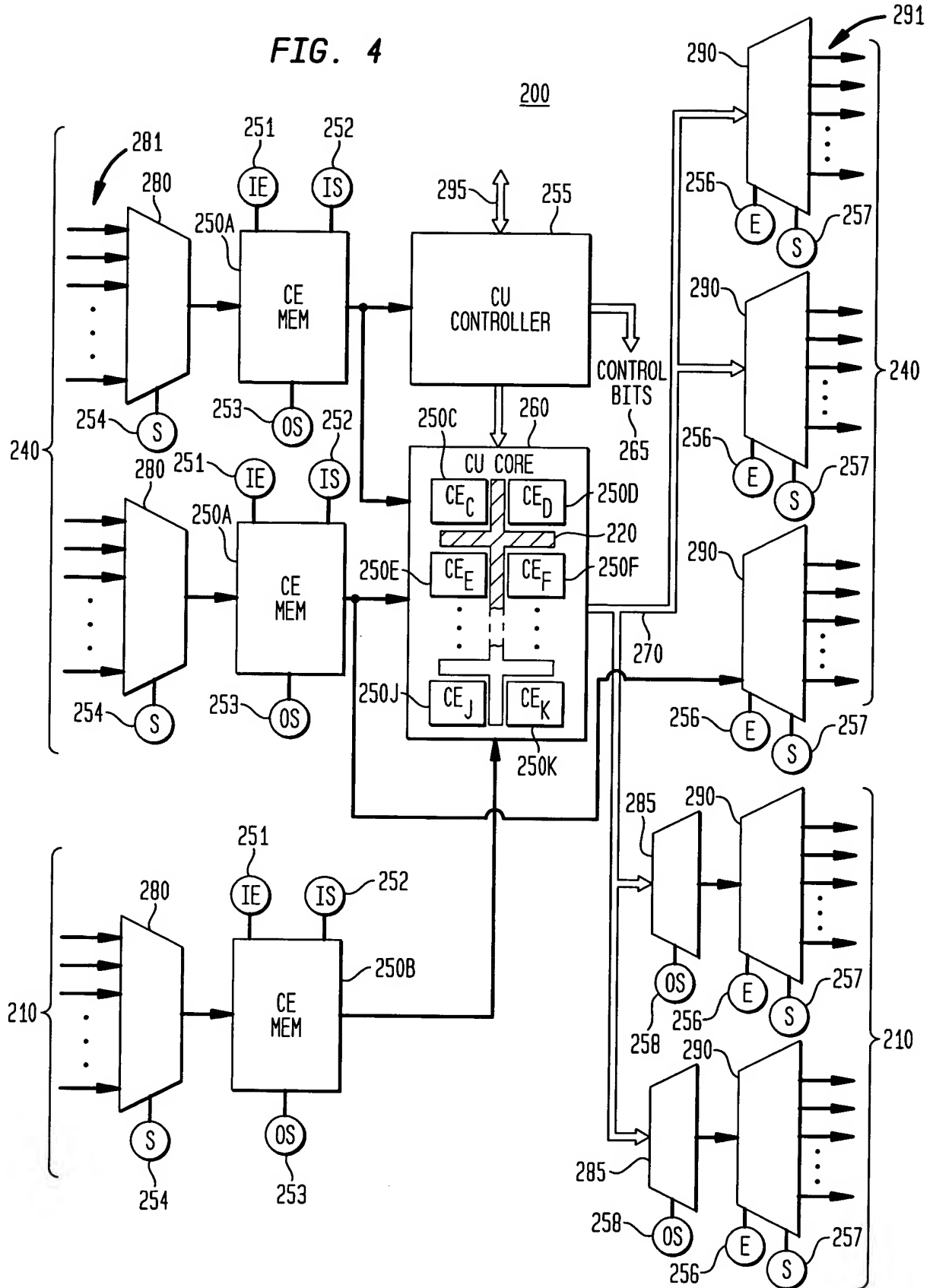


FIG. 5A

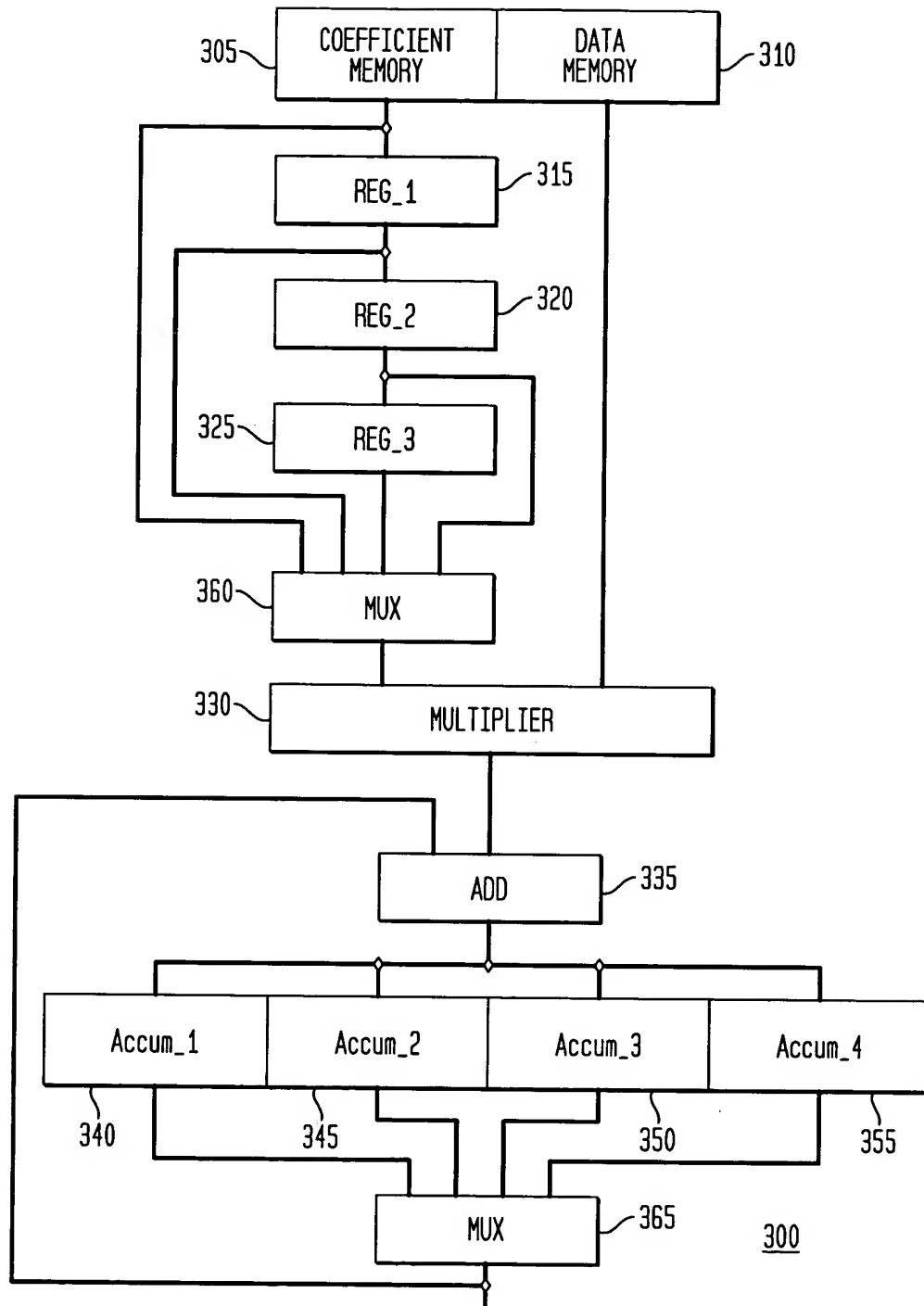


FIG. 5B

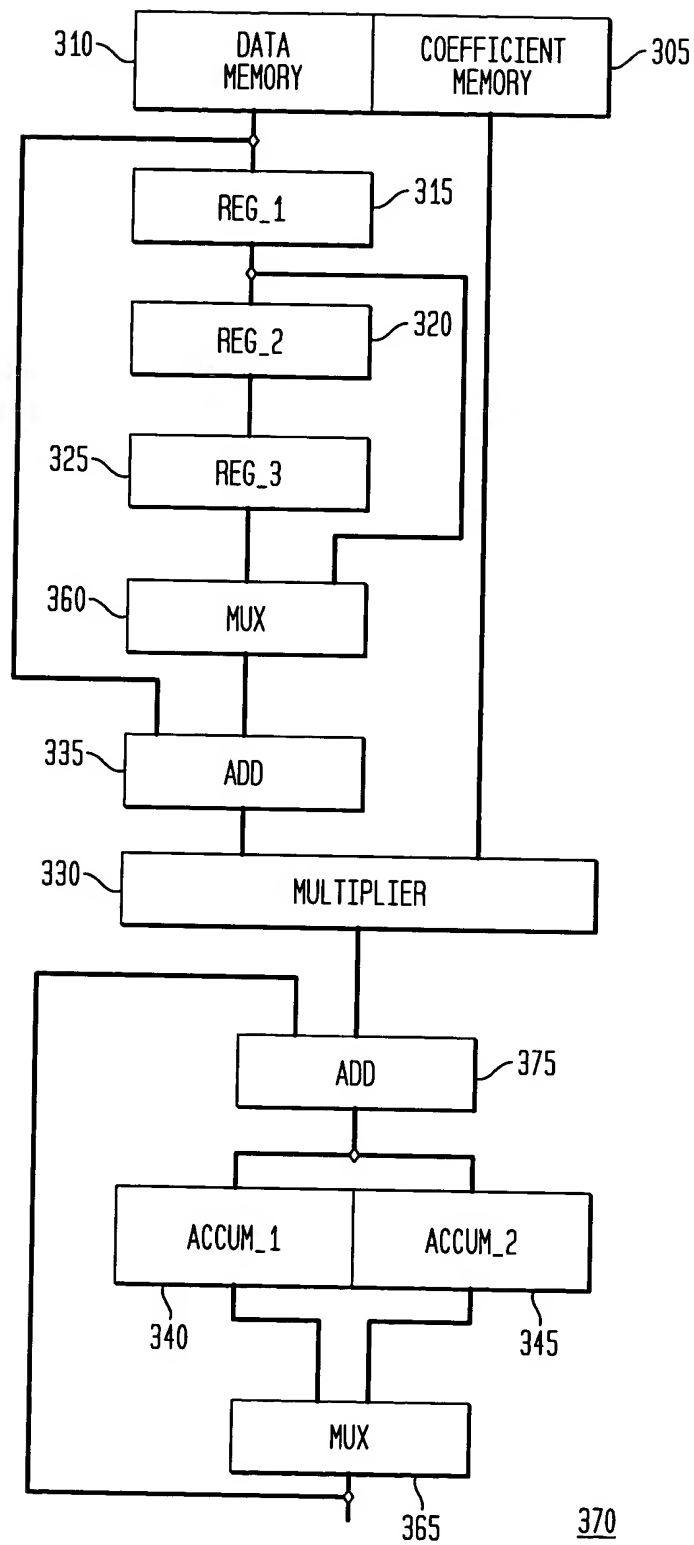


FIG. 5C

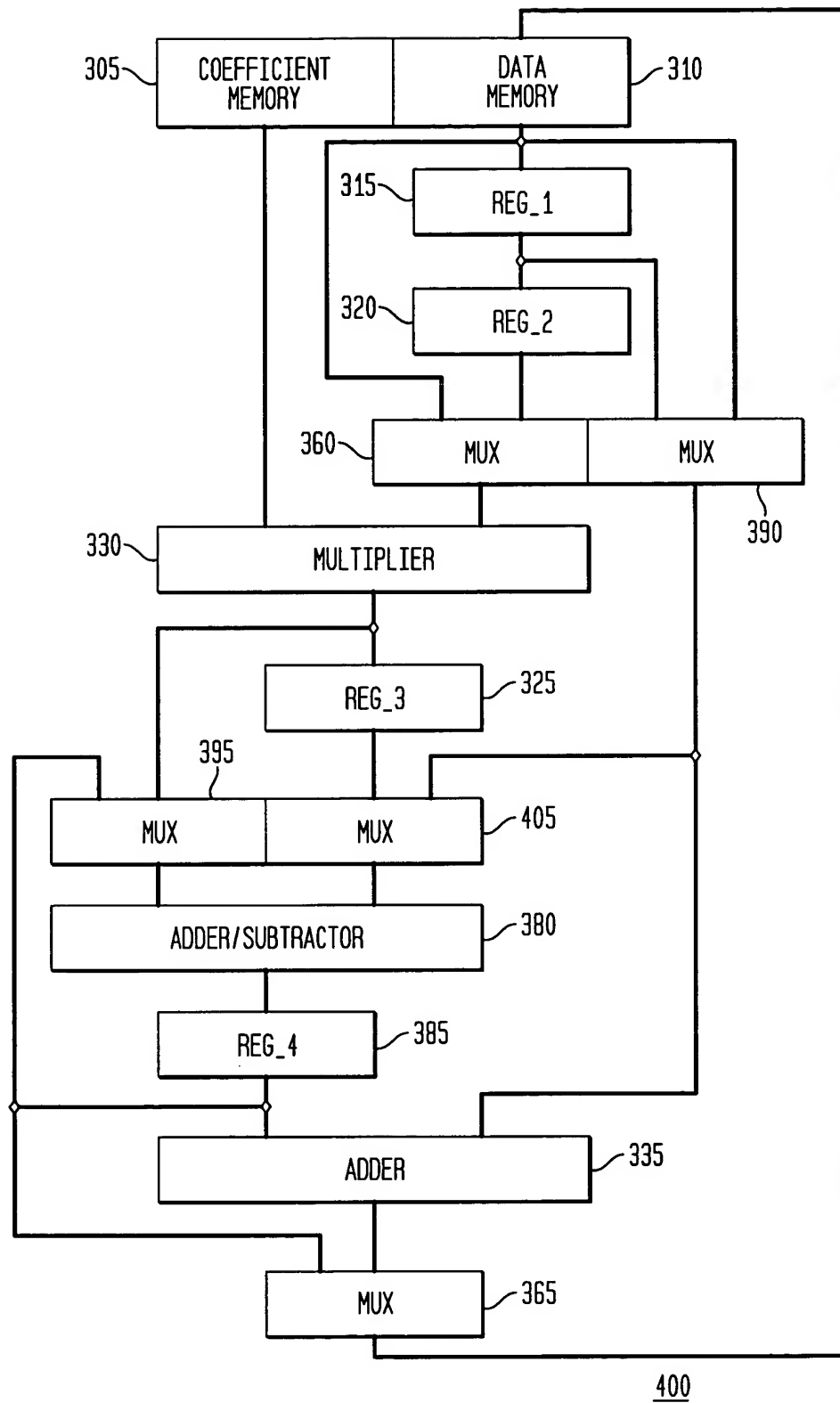
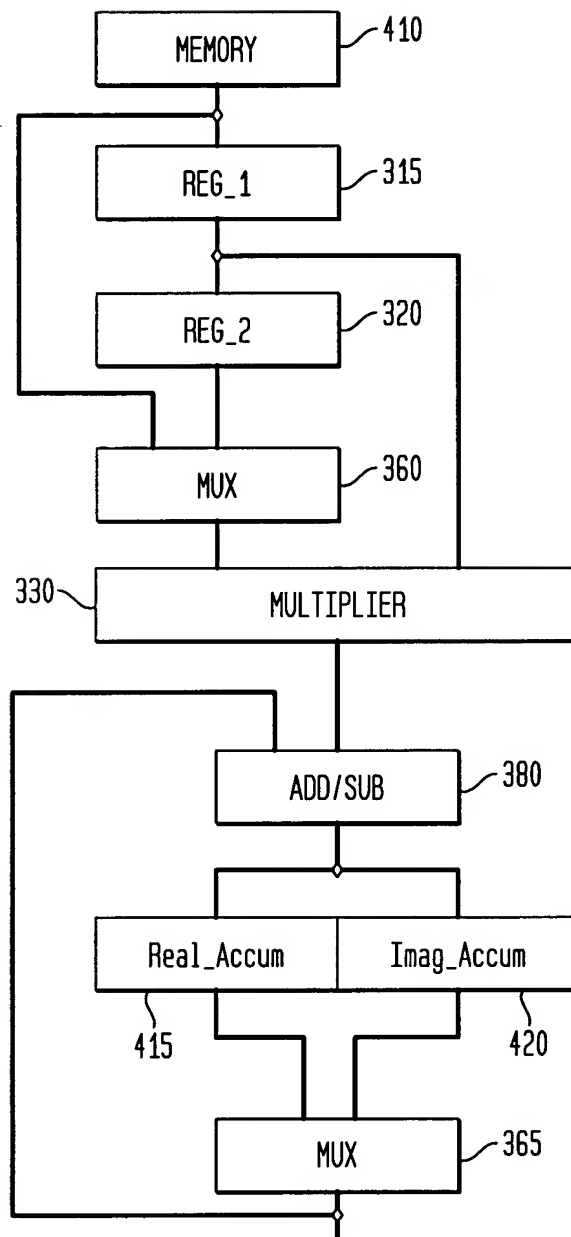
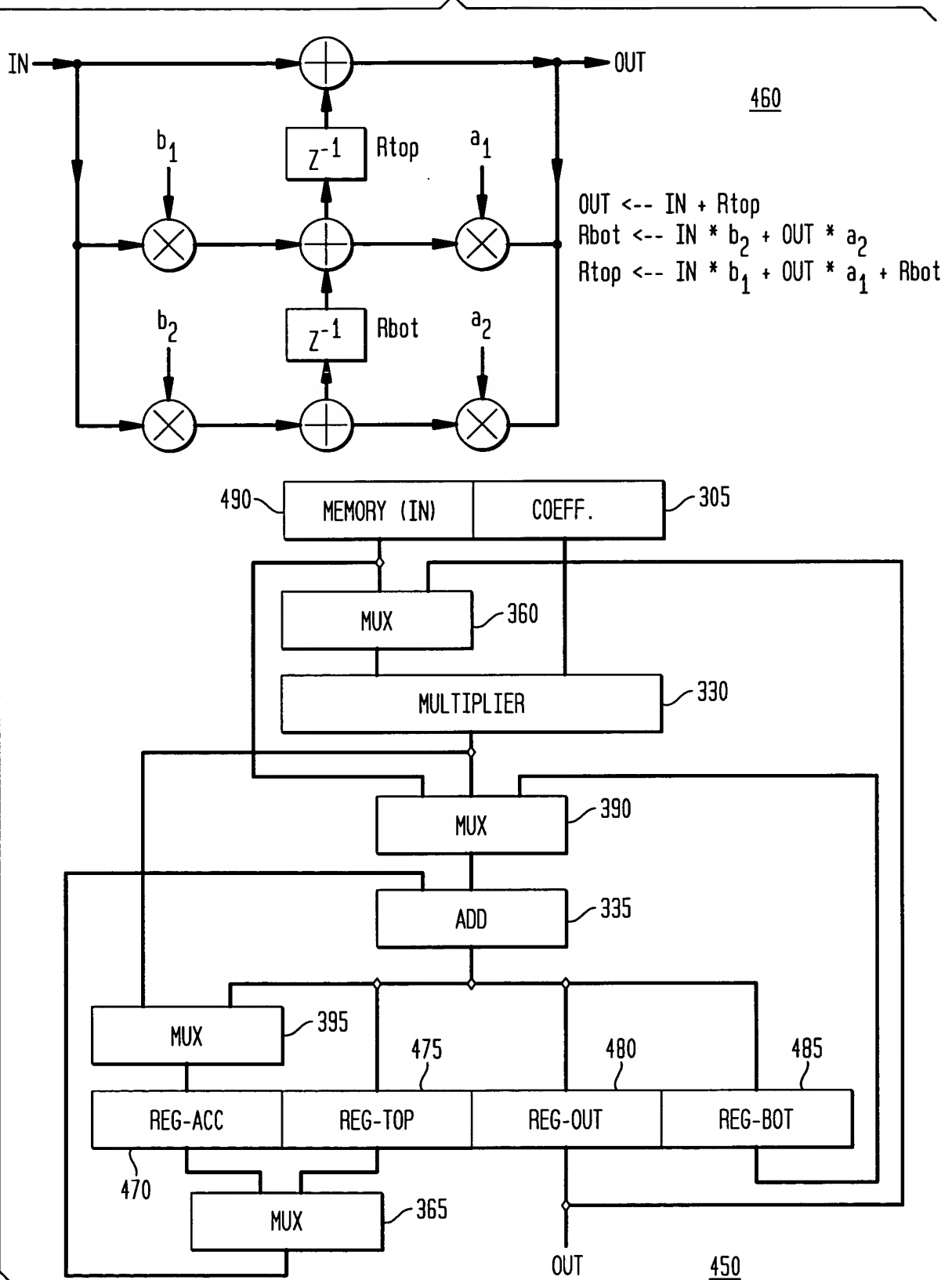


FIG. 5D



9/27
FIG. 5E



10/27

FIG. 6

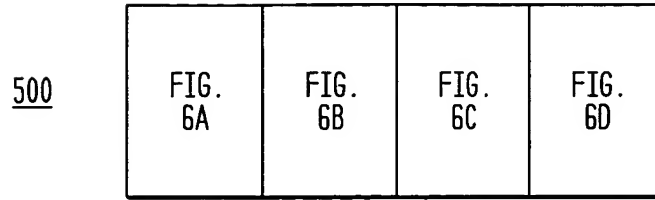
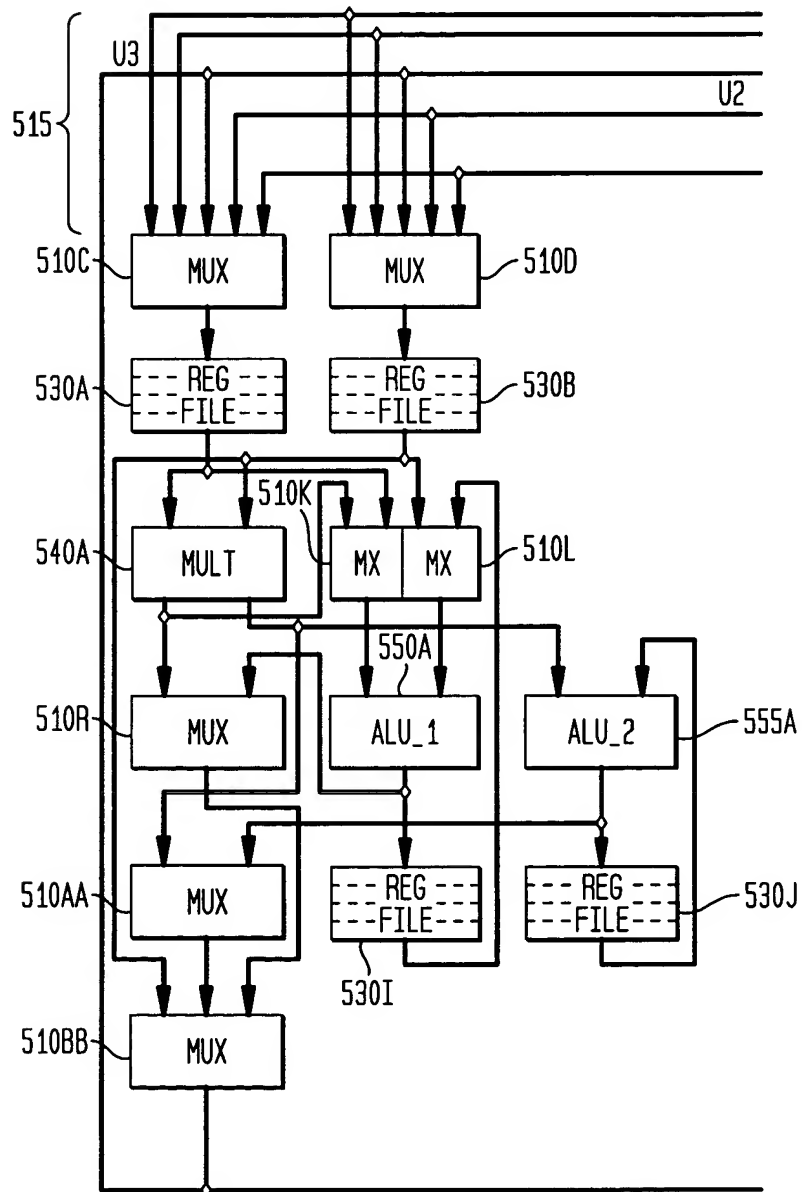


FIG. 6A



11/27

FIG. 6B

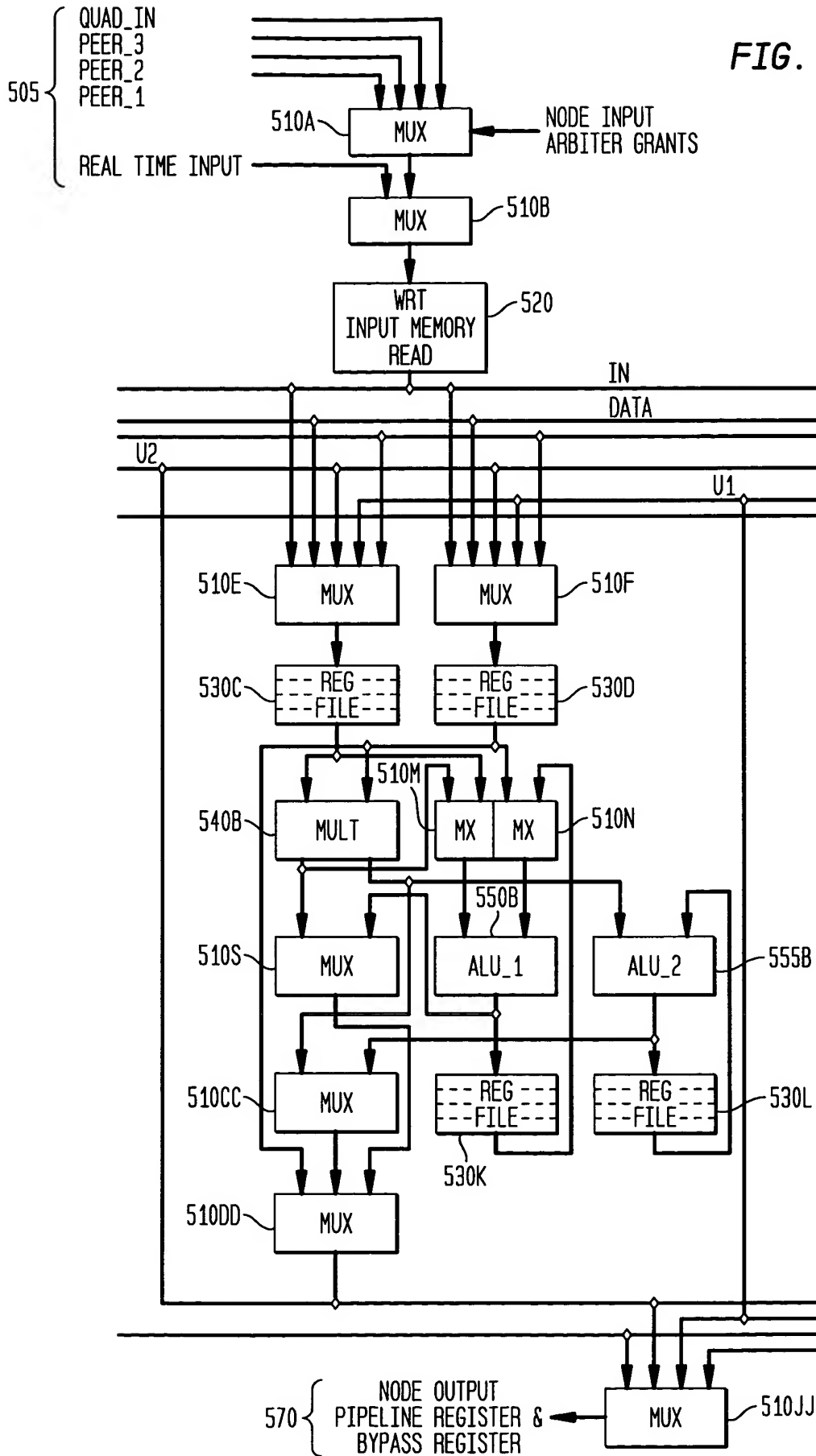
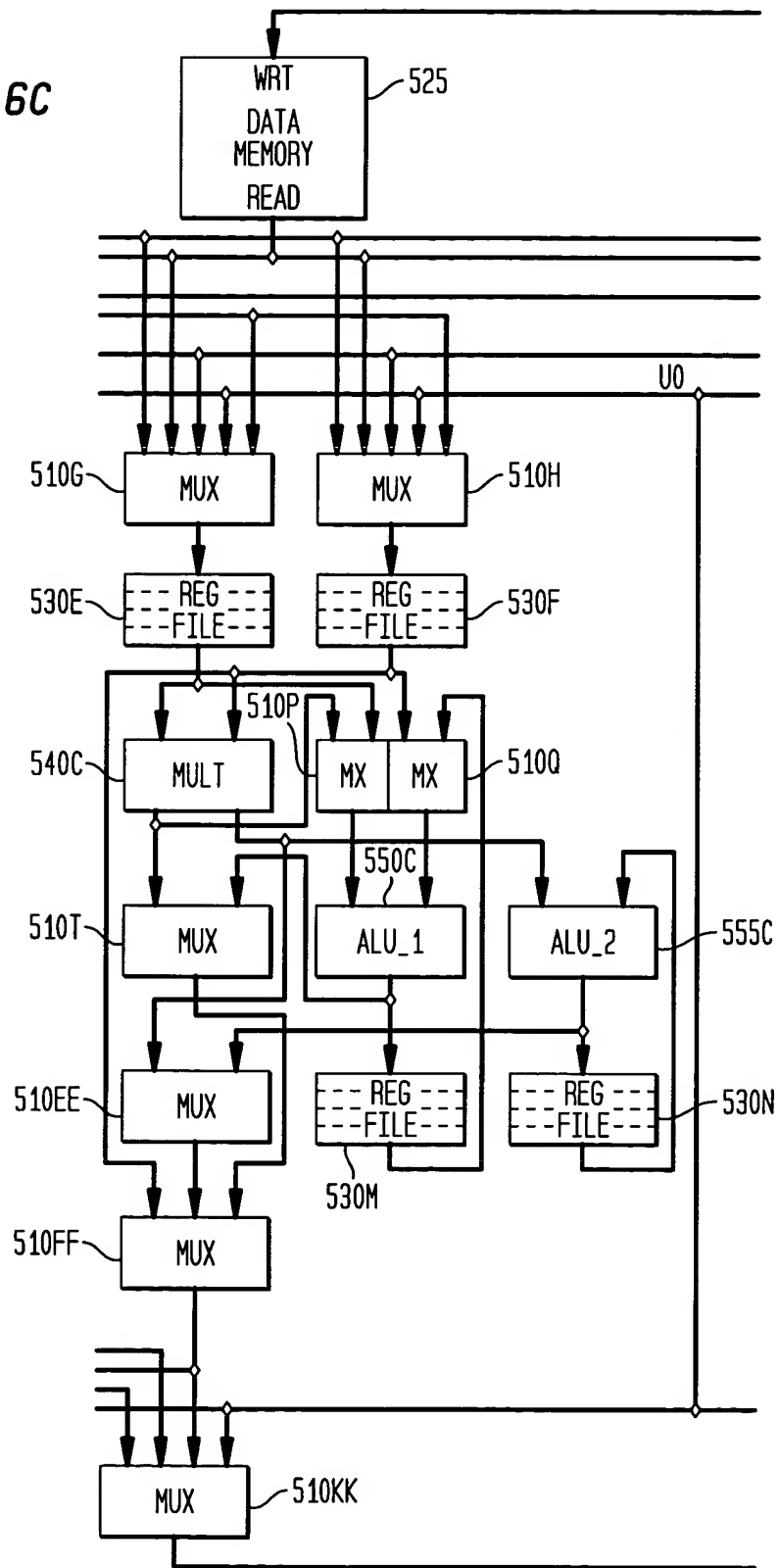
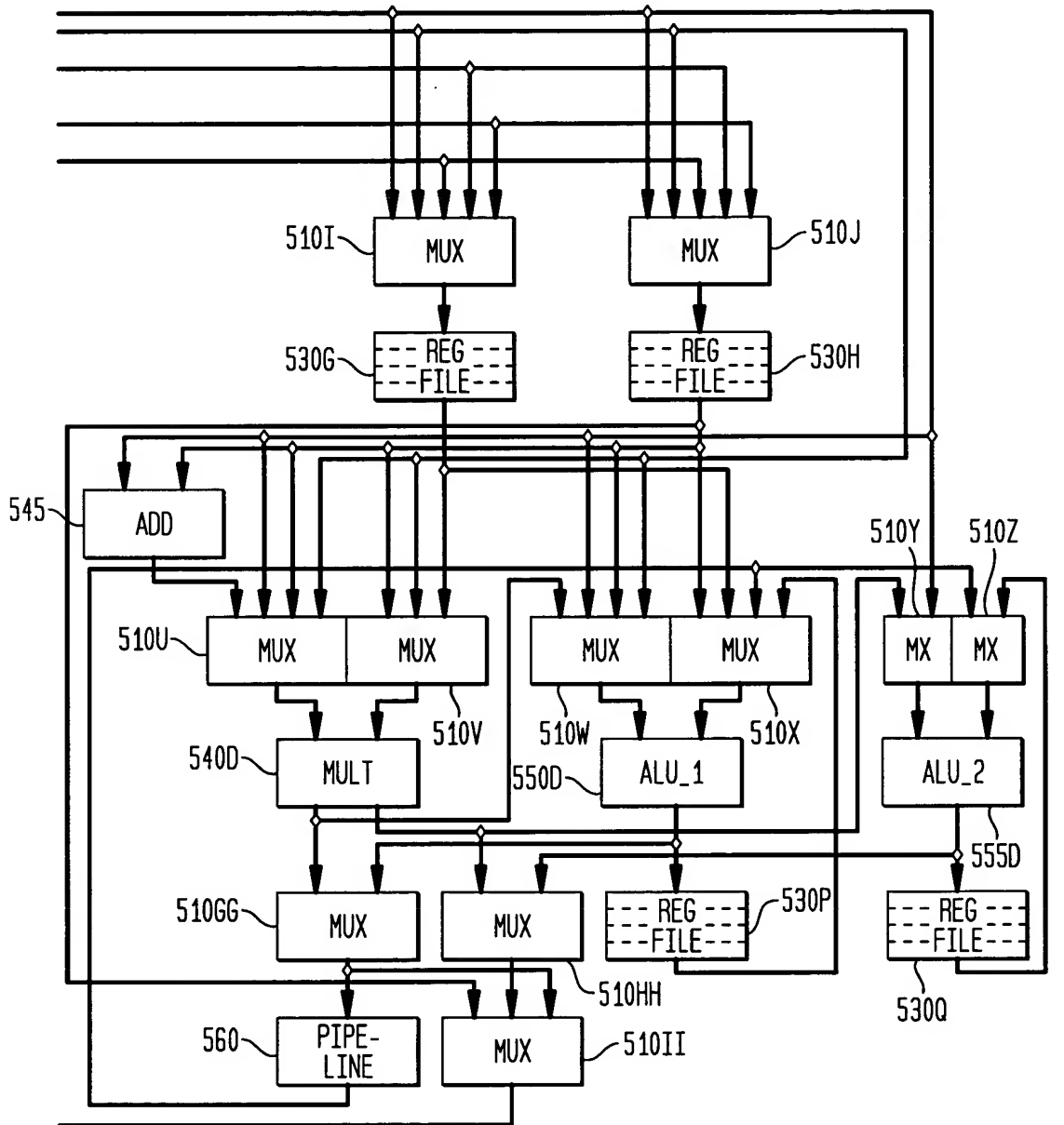


FIG. 6C





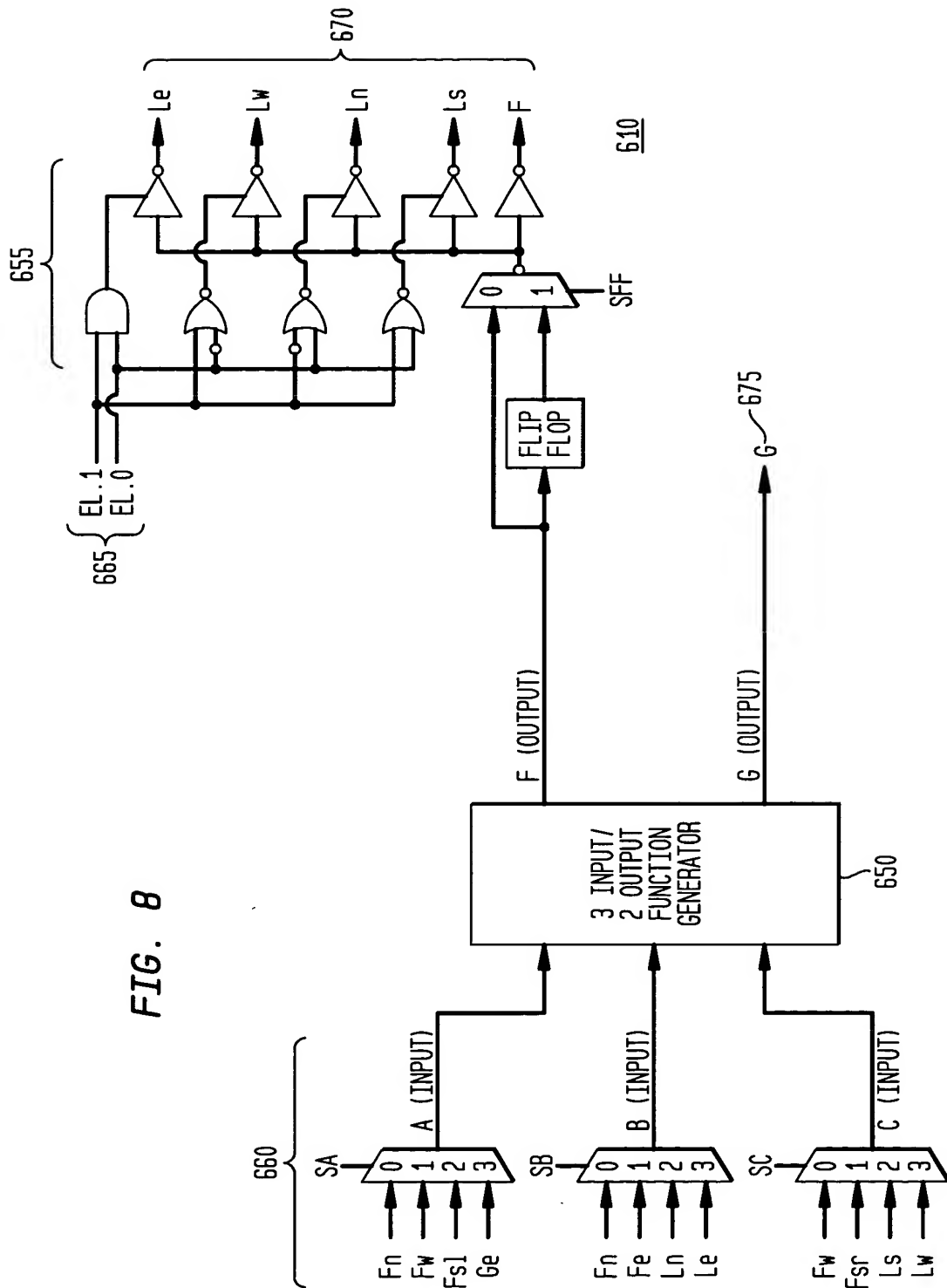


FIG. 9

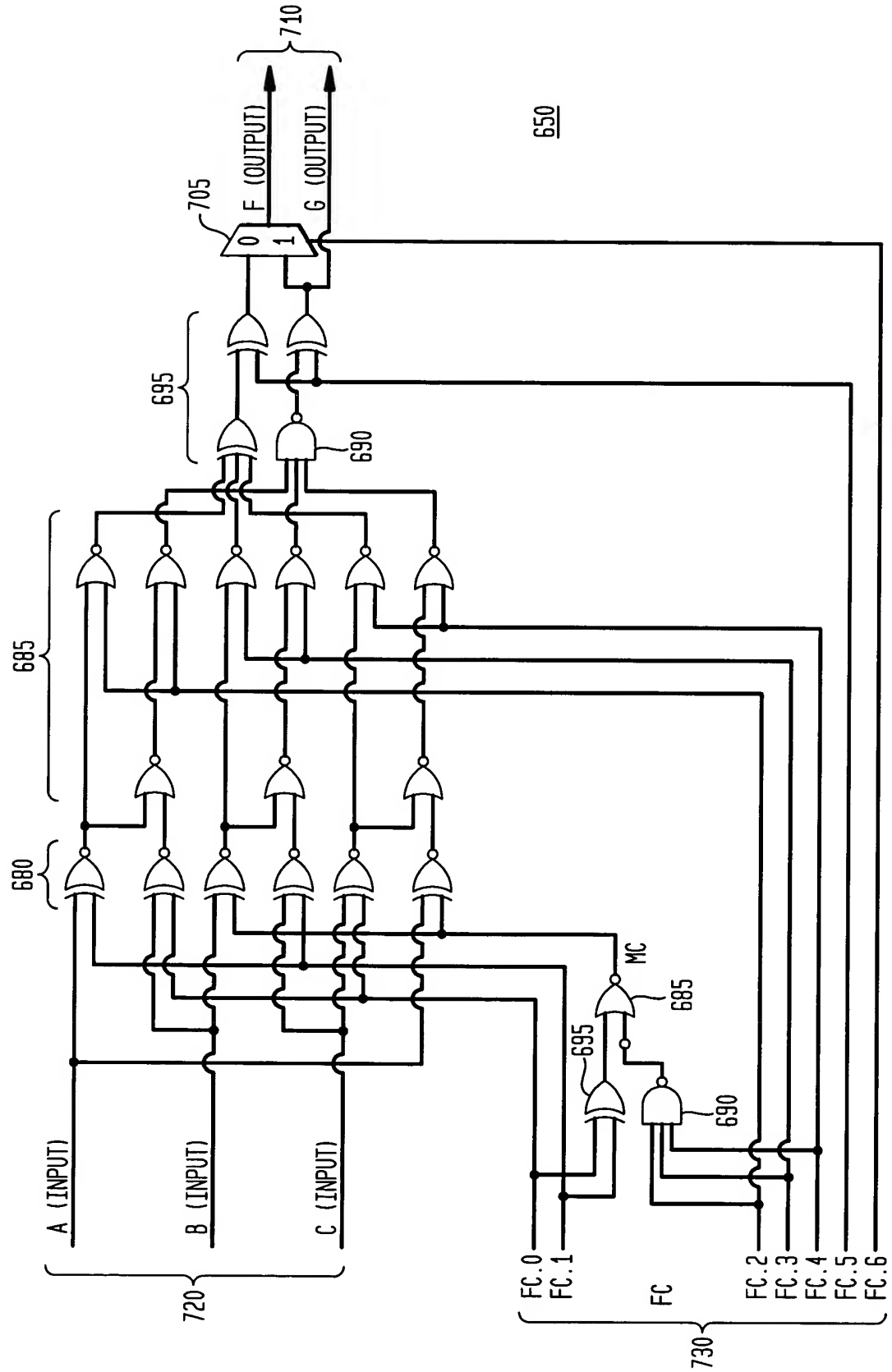


FIG. 10

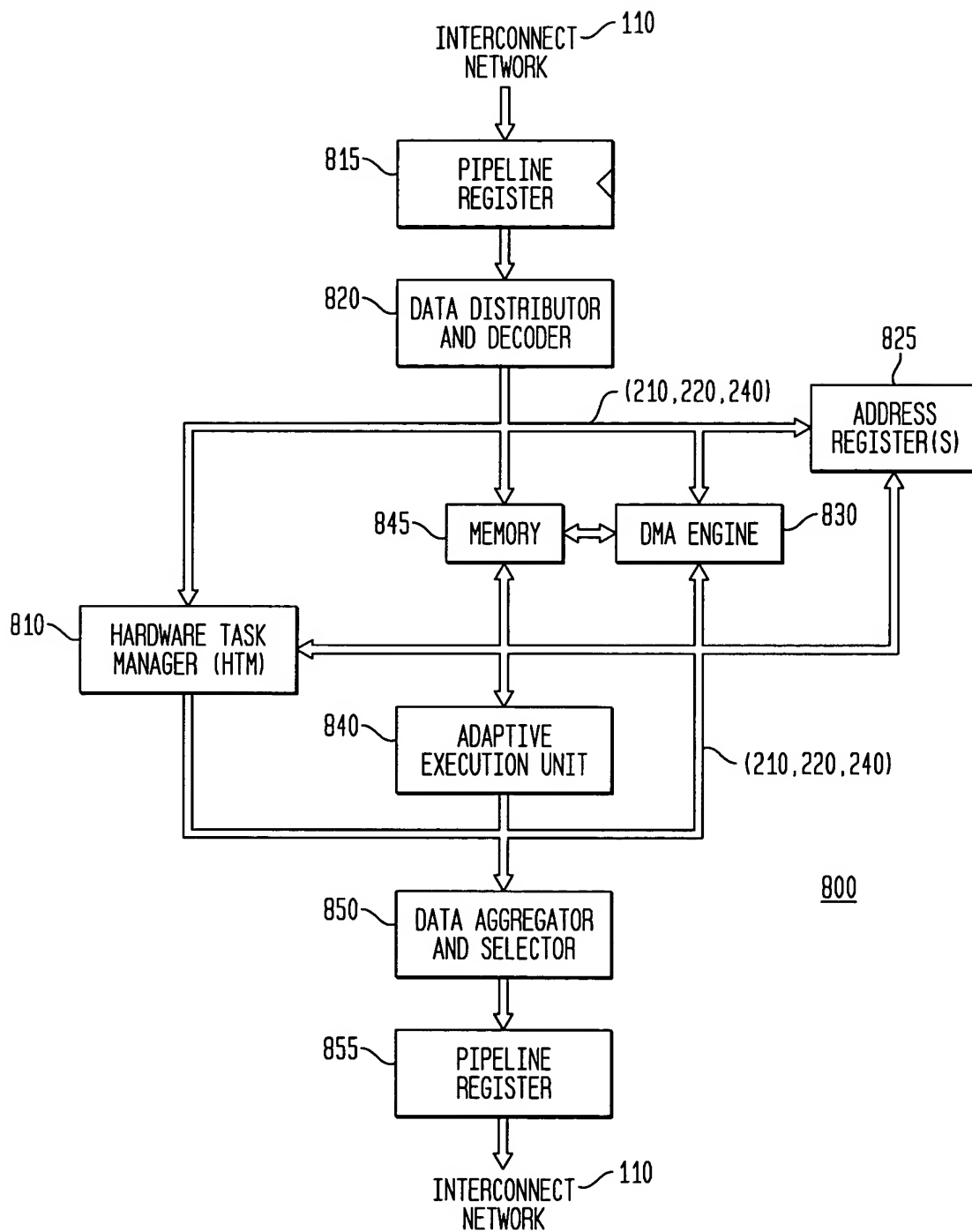


FIG. 11

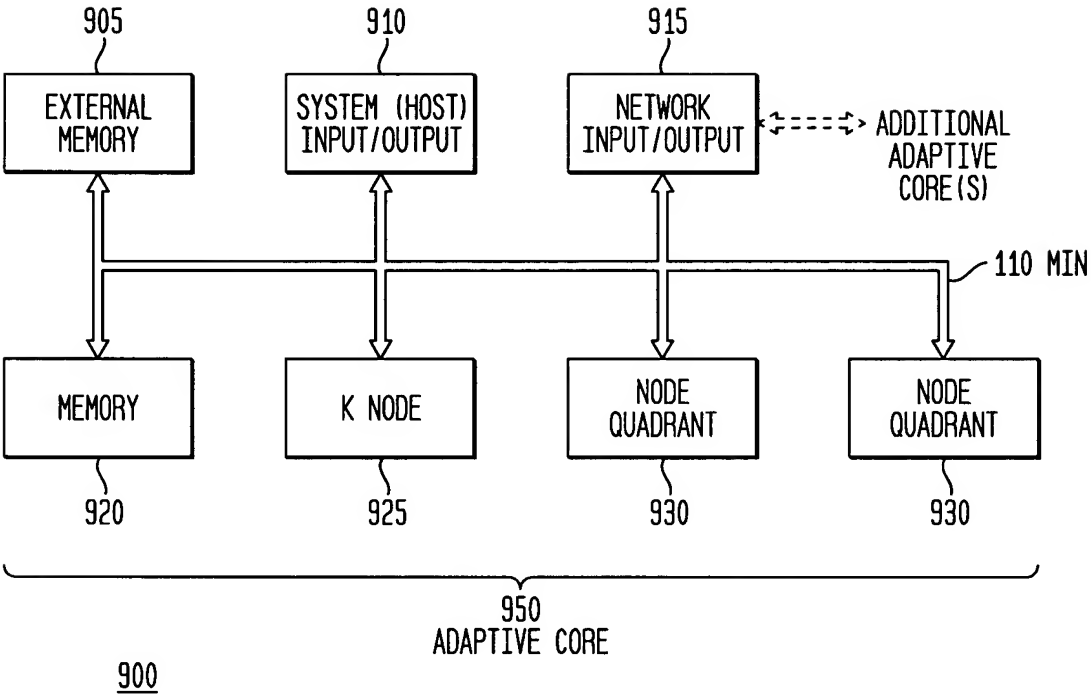


FIG. 12

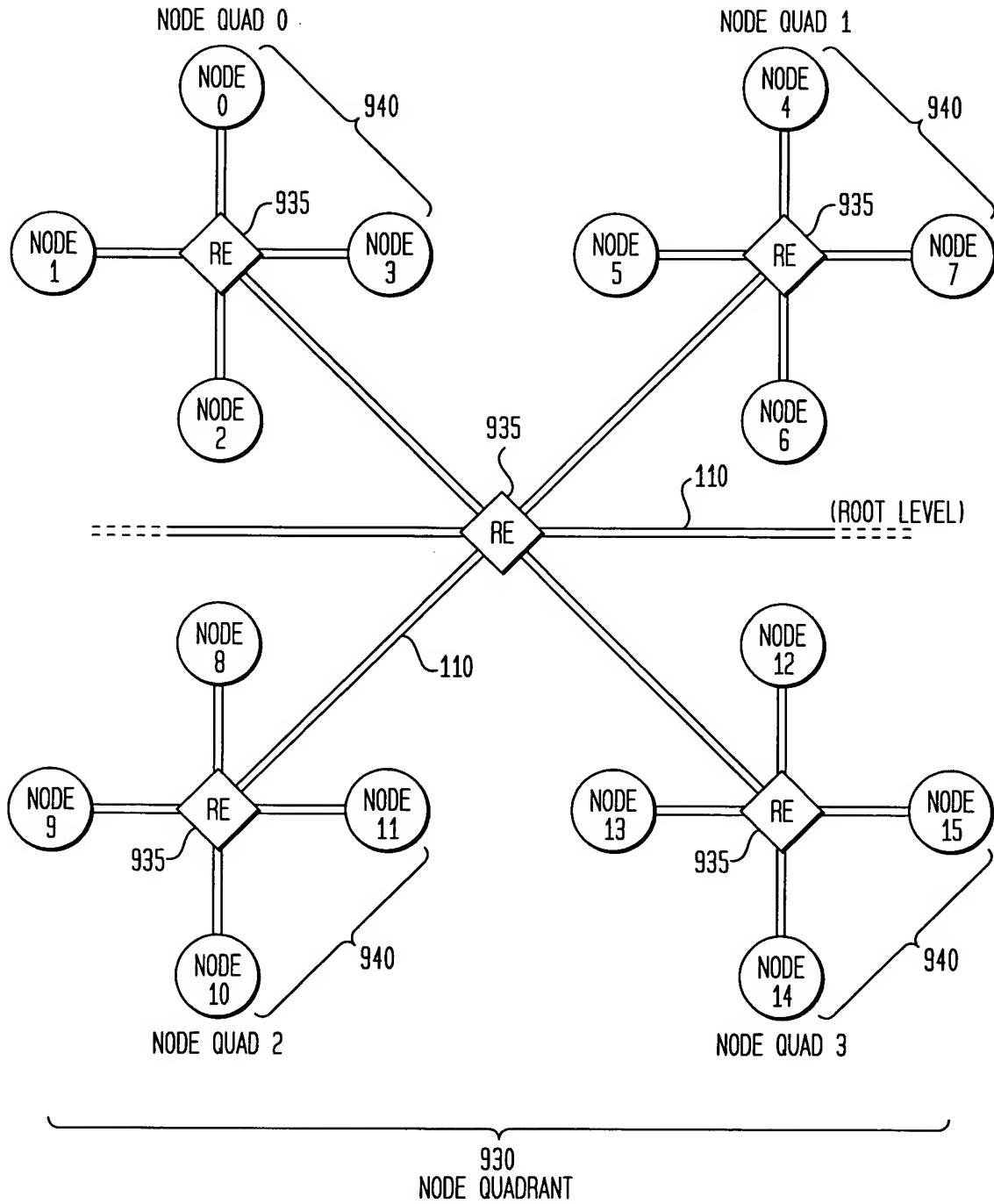


FIG. 13

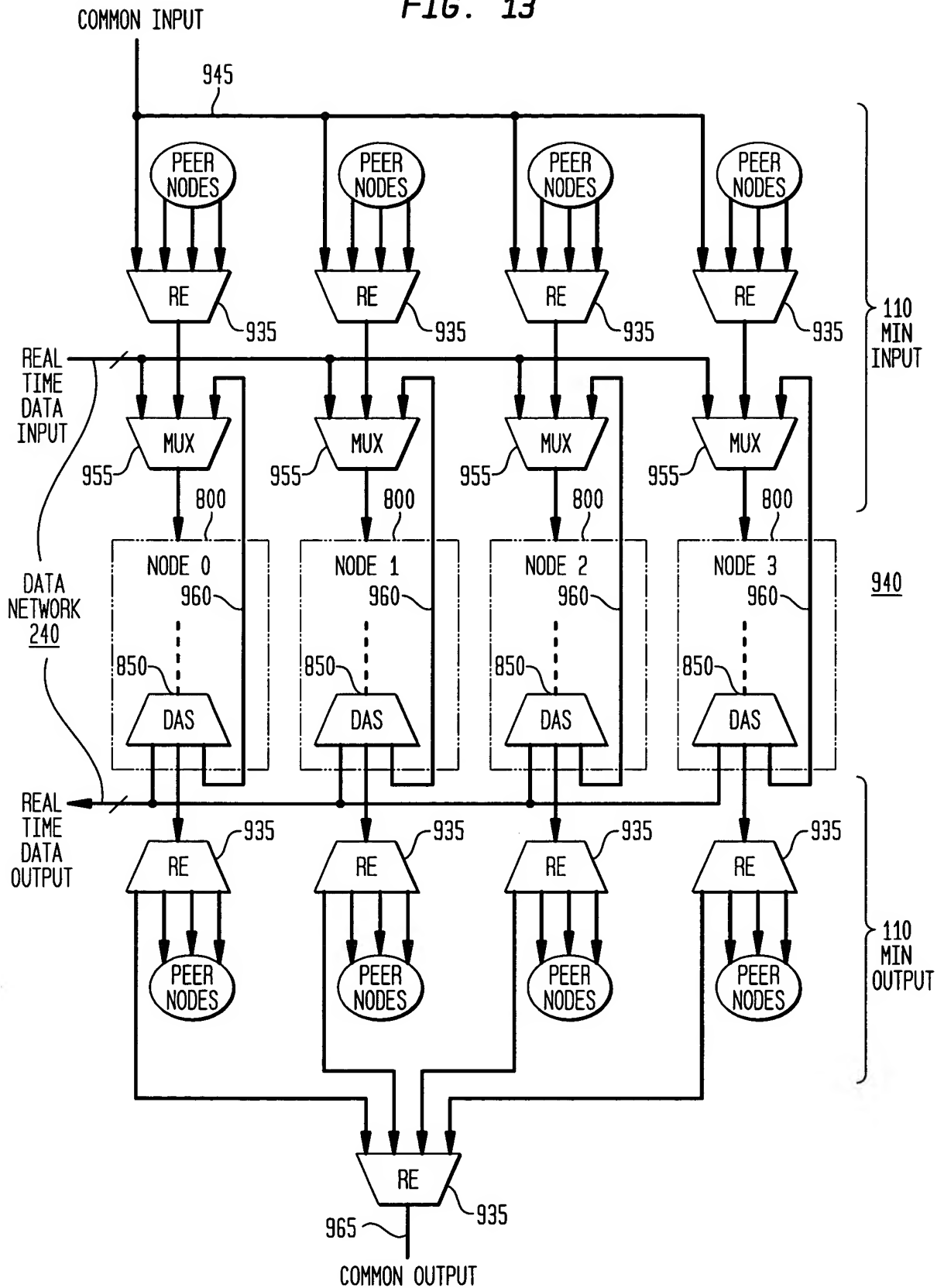


FIG. 14

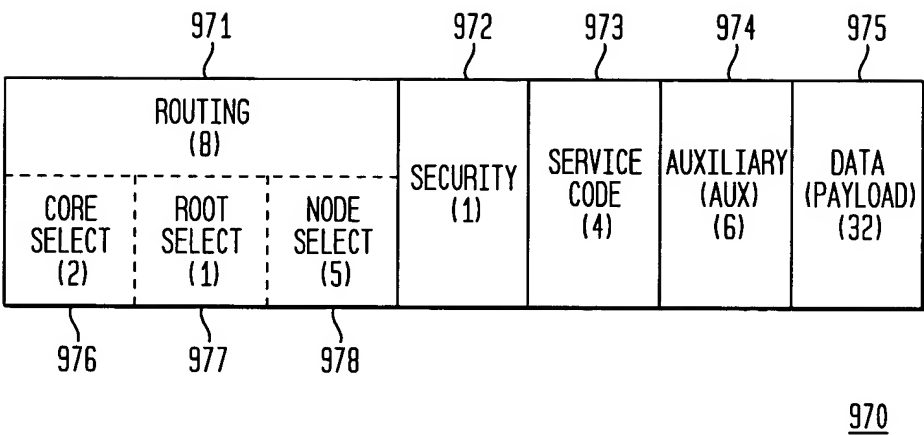


FIG. 15

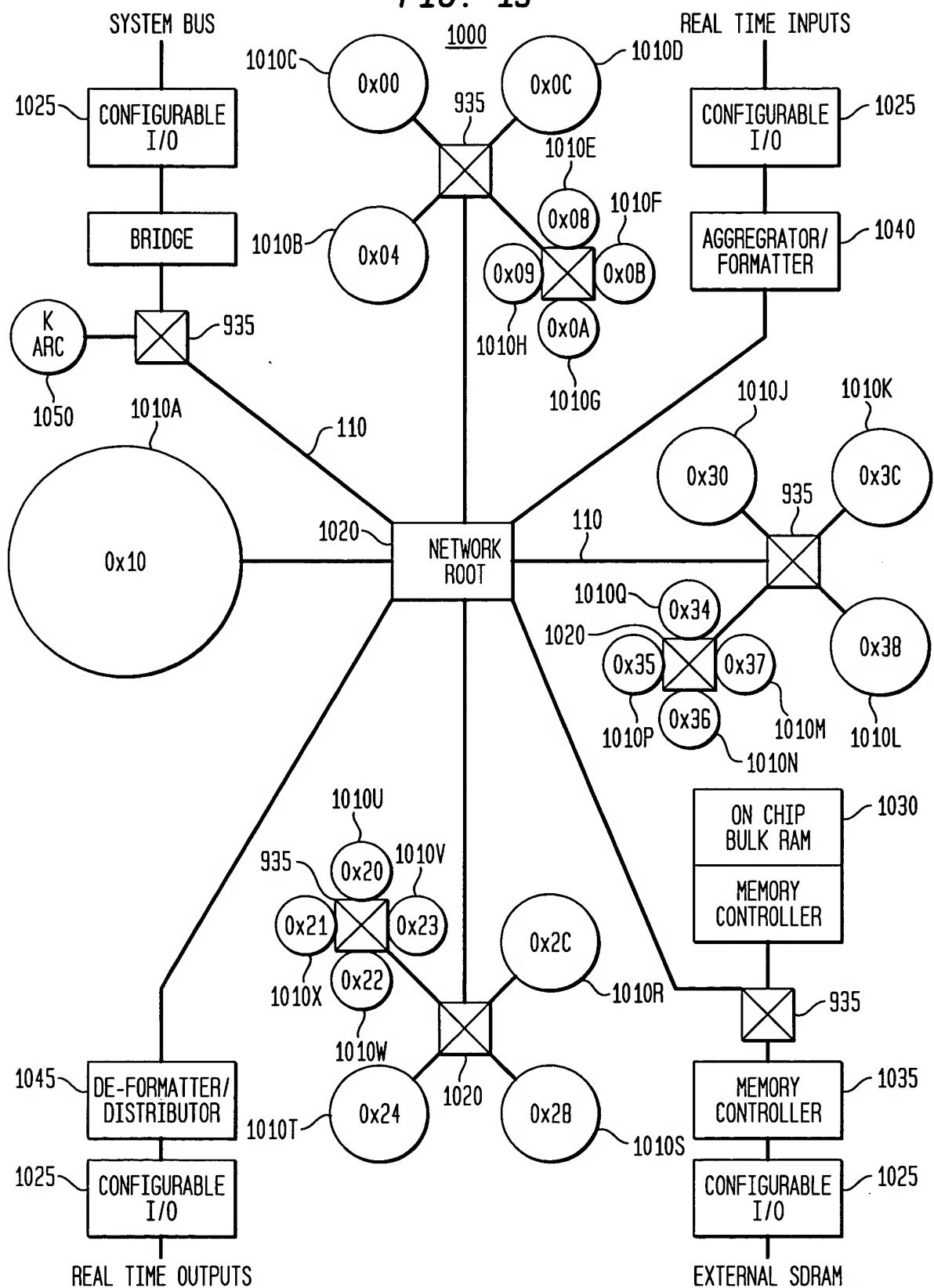


FIG. 16

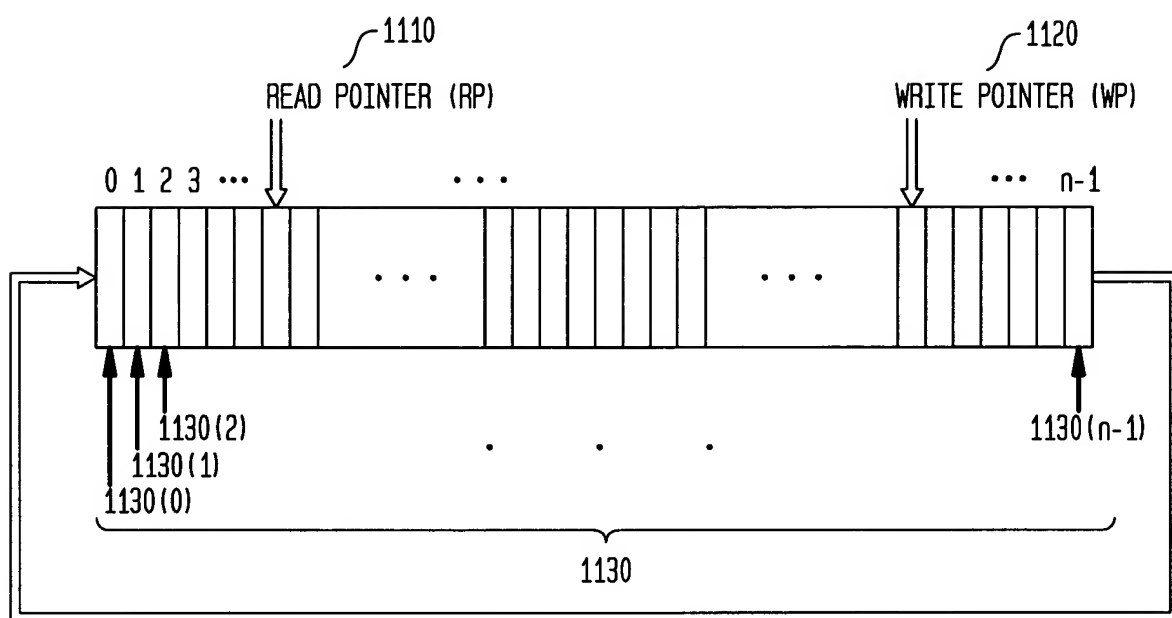
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FIG. 17

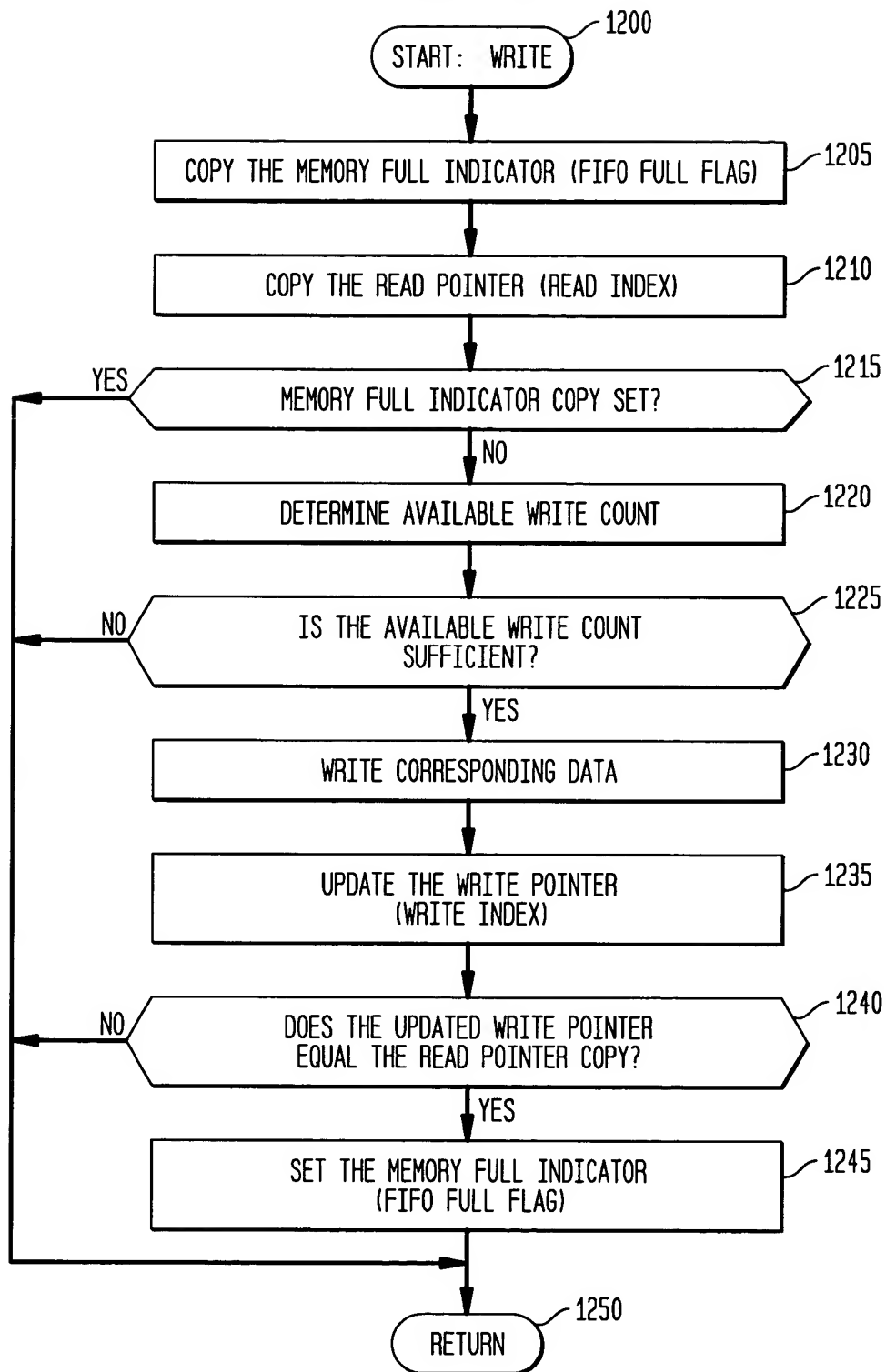
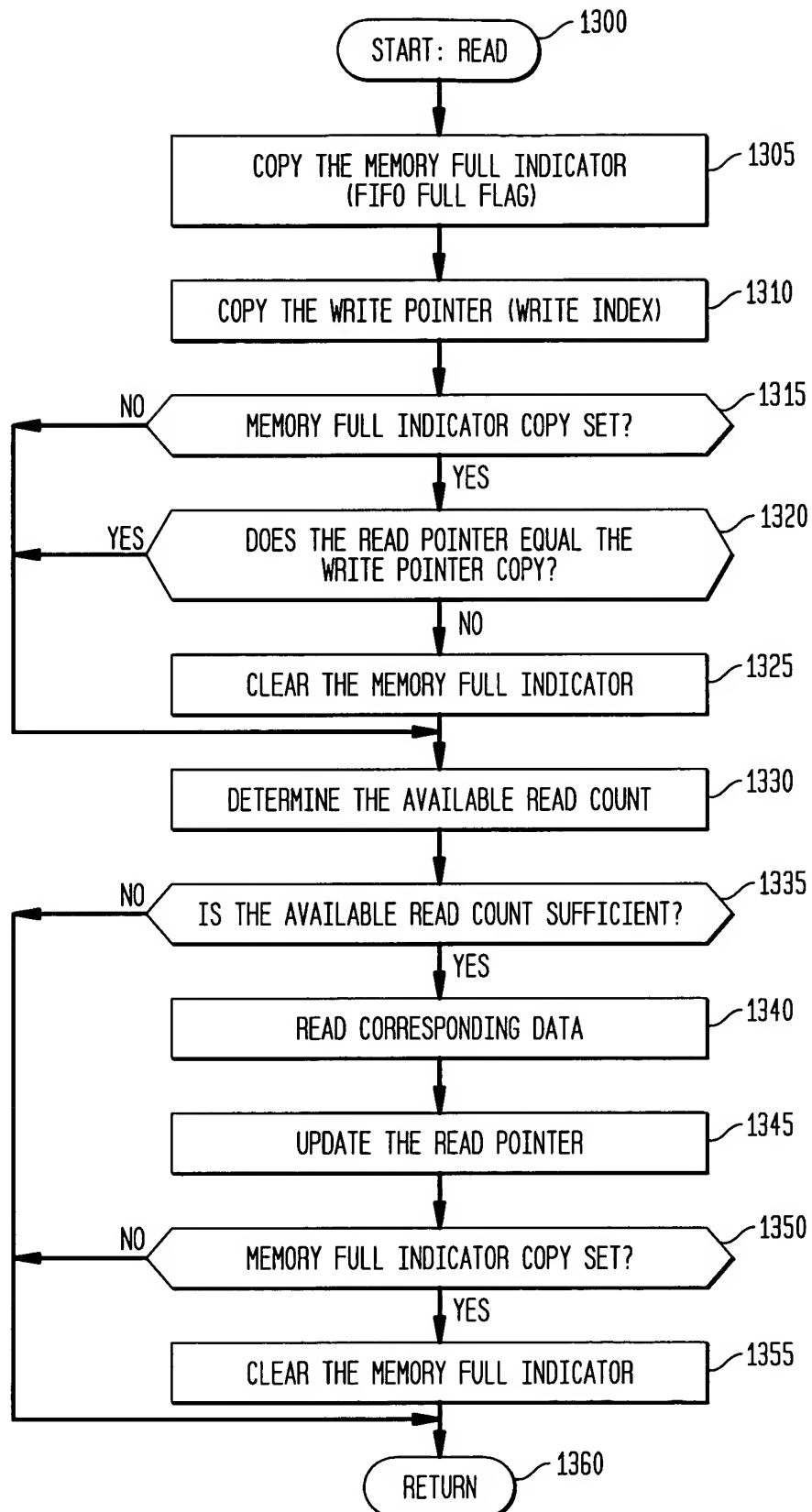


FIG. 18

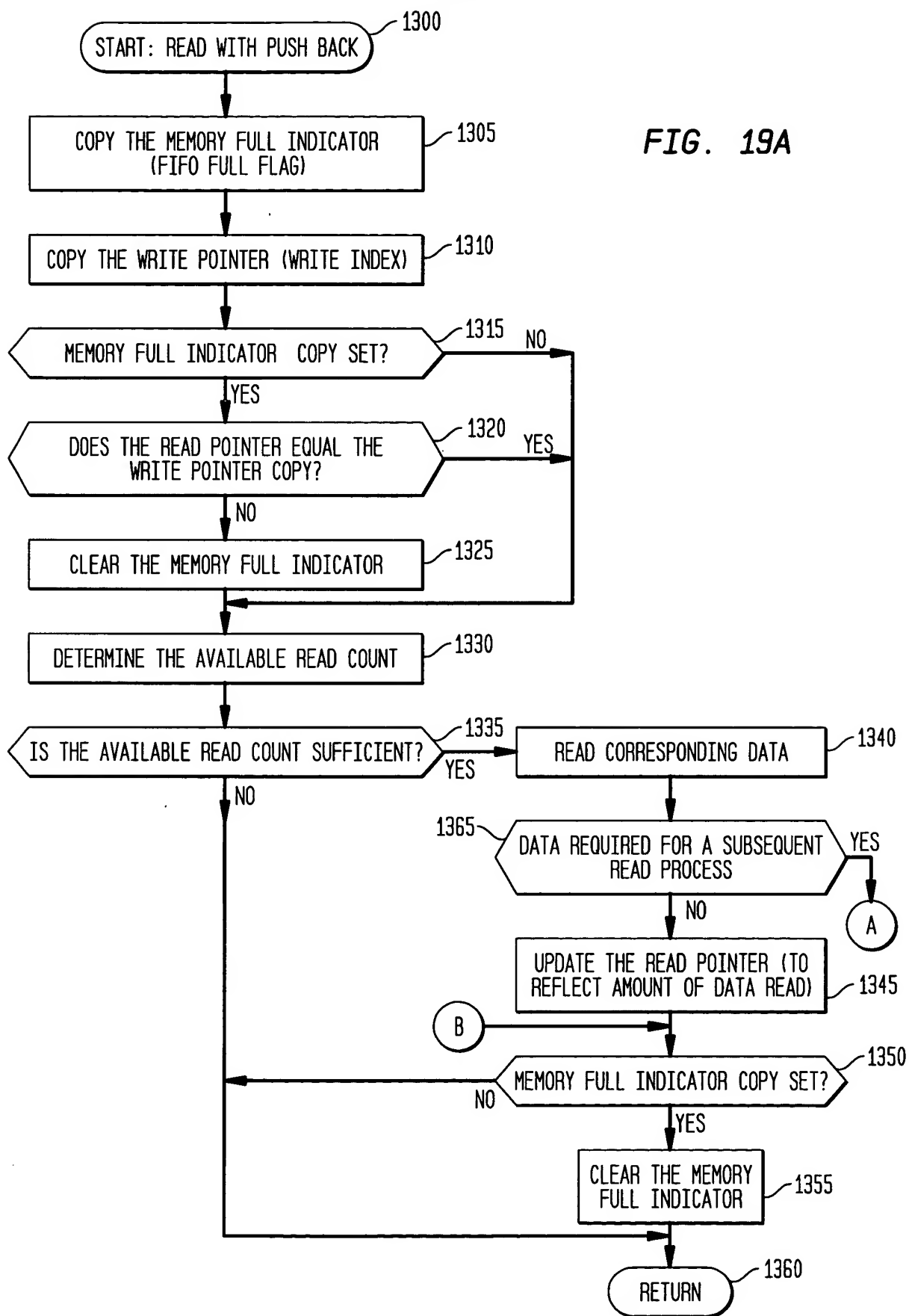


FIG. 19B

